Review of concepts

Network layer’s main function: moving data from one endpoint to another

Analogy: postal system

Forwarding
Data plane

Routing
Control plane

Addressing (IPv4)

Locate, not identify

10000000 11000011 00000001 01010000
128 . 195 . 1 . 80

IP prefixes
==
zip code

Classless (CIDR)

128.195.0.0/20
Review of concepts

- Net layer
- MGR router

Control plane

Data plane

route processor

high-speed switching fabric

router input ports

router output ports
Input port functions

- **Route lookup:** high-speed lookup of which output port the packet is destined to

- **Goal:** must complete this processing at the line rate

- **Queueing:** packets may wait in per-output-port queues if packets are arriving too fast for the switching fabric to send them to the output port
Route lookups

Packet forwarding in the Internet is based on the destination IP address on the packet.

Example: if dst IP on packet is 65.45.145.34, it matches the forwarding table prefix 65.0.0.0/8.
The packet is forwarded out port 3.

Example 2: what about dst IP 128.9.5.6?
Route lookups

Number of entries in the forwarding table matters.
Fitting into router memory
Designing hardware and software for fast lookups
Route lookups

Recall: IP addresses can be aggregated based on shared prefixes.

The number of table entries in a router is proportional to the number of prefixes, NOT the number of endpoints.

Today: ~ 1 million prefixes.

<table>
<thead>
<tr>
<th>Dst-network</th>
<th>Port</th>
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<tbody>
<tr>
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<td>3</td>
</tr>
<tr>
<td>128.9.0.0/16</td>
<td>1</td>
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<tr>
<td>149.12.0.0/19</td>
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Route lookups

Destination-IP-based forwarding has consequences.

• Forwarding behavior is independent of the source: legitimate source vs. malicious attack traffic
• Forwarding behavior is independent of the application: web traffic vs. file download vs. video
• IP-based packet processing is “baked into” router hardware: evolving the IP protocol faces tall deployment hurdles
Output port functions

- Components in reverse order of those in the input port
- This is where most routers have the bulk of their packet buffers
  - Recall discussions regarding router buffers from transport
- MGR uses per-port output buffers, but modern routers have shared memory buffers
  - More efficient use of memory under varying demands
Output port functions

• Two important policy decisions
  • **Scheduling:** which among the waiting packets gets to be transmitted out the link?
    • Ex: First-In-First-Out (FIFO)
  • **Buffer management:** which among the packets arriving from the fabric get space in the packet buffer?
    • Ex: Tail drop: later packets dropped first
Fabrics: Types

Fabric goal: Ferry as many packets as possible from input to output ports as quickly as possible.

Input port writes packets into shared memory. Output port reads the packet when output link ready to transmit.

Single shared channel to move data from input to output port. Easy to build buses; technology is quite mature.

Each input port has a physical data path to every output port. Switch at the cross-over points turns on to connect pairs of ports.
Modern high-speed routers use highly optimized shared-memory-based interconnects.

Crossbars can get expensive as the number of ports grows ($N^2$ connections for $N$ ports).

MGR uses a crossbar and schedules (in,out) port pairs.
Nonblocking fabrics

- High-speed switching fabrics designed to be **nonblocking**:  
  - If an output port is “available”, an input port can always transmit to it without being blocked by the switching fabric itself  
  - Nontrivial to achieve

- Crossbars are nonblocking by design

- Shared memory can be designed to be nonblocking if the memory access is fast enough
Nonblocking fabrics

- With a nonblocking fabric, queues aren’t formed due to the switching fabric.
  - With a nonblocking fabric, there are no queues due to inefficiencies at the input port or the switching fabric.
- Queues only form due to contention for the output port.
  - Fundamental, unavoidable, given the route.
Nonblocking fabrics

- With a nonblocking fabric, queues aren’t formed due to the switching fabric.
  - With a nonblocking fabric, there are no queues due to inefficiencies at the input port or the switching fabric.
- Queues only form **due to contention for the output port**
  - Fundamental, unavoidable, given the route.
- Typically, these queues form on the output side
  - But can also “backpressure” to the input side if there is high contention for the output port.
  - i.e.: can’t move pkts to output Qs since buffers full, so buffer @ input.
Control (plane) processor

- A general-purpose processor that “programs” the data plane:
  - Forwarding table
  - Scheduling and buffer management policy
- Implements the **routing algorithm** by processing routing protocol messages
  - Mechanism by which routers collectively solve the Internet routing problem
  - More on this soon.
Routing Algorithm

Tables. In this example, a routing algorithm runs in each and every router and both forwarding and routing functions are contained within a router. As we'll see in Sections 5.3 and 5.4, the routing algorithm function in one router communicates with the routing algorithm function in other routers to compute the values for its forwarding table. How is this communication performed? By exchanging routing messages containing routing information according to a routing protocol! We'll cover routing algorithms and protocols in Sections 5.2 through 5.4.

The distinct and different purposes of the forwarding and routing functions can be further illustrated by considering the hypothetical (and unrealistic, but technically feasible) case of a network in which all forwarding tables are configured directly by human network operators physically present at the routers. In this case, no routing protocols would be required! Of course, the human operators would need to interact with each other to ensure that the forwarding tables were configured in such a way that packets reached their intended destinations. It's also likely that human configuration would be more error-prone and much slower to respond to changes in the network topology than a routing protocol. We're thus fortunate that all networks have both a forwarding and a routing function!
Longest Prefix Matching
Review: Route lookup

- Table lookup matches a packet against an IP prefix
  - Ex: 65.12.45.2 matches 65.0.0.0/8

- Prefixes are allocated to organizations by Internet registries

- But organizations can reallocate a subset of their IP address allocation to other orgs
Suppose ISP A reallocates a part of its IP block to orgs 1… 8.

Example of IP block reallocation

ISP A owns the IP block 200.23.16.0/20.

Route Aggregation
Save forwarding table memory
Fewer routing protocol msgs

There is an announcement mechanism (BGP) by which ISP A can inform the rest of the Internet about the prefixes it owns. It is enough to announce a coarse-grained prefix 200.23.16.0/20 rather than 8 separate sub-prefixes.

Example of IP block reallocation

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Suppose ISP A reallocates a part of its IP block to orgs 1… 8

ISP A owns the IP block 200.23.16.0/20.

Now suppose one of these organizations adds another ISP for its Internet service and prefers using the new ISP. Note: it’s possible for the organization to retain its assigned IP block.
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ISP A owns the IP block 200.23.16.0/20.

ISP B

[BGP] Announce 200.23.18.0/23
(besides other IP prefixes)

[BGP] Send me pkts destined to 200.23.16.0/20
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[BGP] Send me pkts destined to 200.23.16.0/20
A closer look at the forwarding table

- 200.23.18.0/23 is inside 200.23.16.0/20

- A packet with destination IP address 200.23.18.xx is in both prefixes
  - i.e., both entries match

- Q: How should the router choose to forward the packet?
  - The org prefers B, so should choose B

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The Internet uses a policy to prioritize: Longest Prefix Matching
Longest Prefix Matching (LPM)

- Use the **longest** matching prefix, i.e., the most **specific** route, among all prefixes that match the packet.

- Policy borne out of the Internet’s IP allocation model: prefixes and sub-prefixes are handed out

- Internet routers use longest prefix matching.
  - Very interesting algorithmic problems
  - Challenges in designing efficient software and hardware data structures

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Internet routers perform longest-prefix matching on destination IP addresses of packets.
Why is LPM prevalent?

• An ISP (e.g., Verizon) has allocated a sub-prefix (or “subnet”) of a larger prefix that the ISP owns to an organization (e.g., Rutgers)

• Further, the ISP announces the aggregated prefix to the Internet to save on number of forwarding table memory and number of announcements

• The organization (e.g., Rutgers) is reachable over multiple paths (e.g., through another ISP like AT&T)

• The organization has a preference to use one path over another, and expresses this by announcing the longer (more specific) prefix

• Routers in the Internet must route based on the longer prefix