# The Non-Hardness of Approximating Circuit Size

Eric Allender\* Rahul Ilango<sup>†</sup> Neekon Vafa<sup>‡</sup>
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#### Abstract

The Minimum Circuit Size Problem (MCSP) has been the focus of intense study recently; MCSP is hard for SZK under rather powerful reductions [4], and is provably not hard under "local" reductions computable in TIME( $n^{0.49}$ ) [26]. The question of whether MCSP is NP-hard (or indeed, hard even for small subclasses of P) under some of the more familiar notions of reducibility (such as many-one or Turing reductions computable in polynomial time or in  $AC^0$ ) is closely related to many of the longstanding open questions in complexity theory [7, 8, 19, 20, 21, 23, 26].

All prior hardness results for MCSP hold also for computing somewhat weak approximations to the circuit complexity of a function [3, 4, 10, 19, 24, 31]. Some of these results were proved by exploiting a connection to a notion of time-bounded Kolmogorov complexity (KT) and the corresponding decision problem (MKTP). More recently, a new approach for proving improved hardness results for MKTP was developed [5, 7], but this approach establishes only hardness of extremely good approximations of the form 1 + o(1), and these improved hardness results are not yet known to hold for MCSP. In particular, it is known that MKTP is hard for the complexity class DET under nonuniform  $\leq_{\rm m}^{\rm AC^0}$  reductions, implying MKTP is not in AC<sup>0</sup>[p] for any prime p [7]. It was still open if similar circuit lower bounds hold for MCSP. (But see [14, 22].) One possible avenue for proving a similar hardness result for MCSP would be to improve the hardness of approximation for MKTP beyond 1 + o(1) to  $\omega(1)$ , as KT-complexity and circuit size

<sup>\*</sup>Rutgers University, Piscataway, NJ, USA allender@cs.rutgers.edu

<sup>&</sup>lt;sup>†</sup>Massachusetts Institute of Technology, Cambridge, MA, USA rilango@mit.edu

<sup>&</sup>lt;sup>‡</sup>Harvard University, Cambridge, MA, USA and Google, Mountain View, CA, USA neekonvafa@gmail.com

<sup>&</sup>lt;sup>1</sup>Subsequent to our work, a new hardness result has been announced [22] that relies on more exact size computations.

are polynomially-related. In this paper, we show that this approach cannot succeed.

More specifically, we prove that PARITY does not reduce to the problem of computing superlinear approximations to KT-complexity or circuit size via  $AC^0$ -Turing reductions that make O(1) queries. This is significant, since approximating any set in P/poly  $AC^0$ -reduces to just one query of a much worse approximation of circuit size or KT-complexity [28]. For weaker approximations, we also prove non-hardness under more powerful reductions. Our non-hardness results are unconditional, in contrast to conditional results presented in [7] (for more powerful reductions, but for much worse approximations). This highlights obstacles that would have to be overcome by any proof that MKTP or MCSP is hard for NP under  $AC^0$  reductions. It may also be a step toward confirming a conjecture of Murray and Williams, that MCSP is not NP-complete under logtime-uniform  $\leq_{AC^0}^{AC^0}$  reductions.

### 1 Introduction

The Minimum Circuit Size Problem (MCSP) is the problem of determining whether a (given) Boolean function f (represented as a bitstring of length  $2^k$  for some k) has a circuit of size at most a (given) threshold  $\theta$ . Although the complexity of MCSP has been studied for more than half a century (see [32, 24] for more on the history of the problem), recent interest in MCSP traces back to the work of Kabanets and Cai [24], who connected the problem to questions involving the natural proofs framework of Razborov and Rudich [30].

Since then, there has been a flurry of research on MCSP [3, 4, 5, 6, 7, 8, 18, 19, 20, 21, 23, 26, 28], but still the exact complexity of MCSP remains unknown. MCSP is in NP, but it remains an important open question whether MCSP is NP-complete.

MCSP is likely not in P. There is good evidence for believing MCSP  $\not\in$  P. If MCSP is in P, then there are no cryptographically-secure one-way functions [24]. Furthermore, [4] shows MCSP is hard for SZK under BPP-Turing reductions, so if MCSP  $\in$  P then SZK  $\subseteq$  BPP, which seems unlikely.

Showing MCSP is NP-hard would be difficult. Murray and Williams [26] have shown that if MCSP is NP-hard under polynomial-time many-one reductions, then  $EXP \neq ZPP$ , which is a likely separation but one that escapes current techniques. Results from [4, 21, 26] also give various likely (but difficult to show) consequences for MCSP being hard under more restrictive

forms of reduction. We note that it has been suggested that MCSP might well be complete for NP [23]. In this regard, it may also be relevant to note that MCSPQBF is complete for PSPACE under ZPP-Turing reductions [3].

The hardness of both MCSP and approximating MCSP have important consequences for complexity theory. We have already mentioned that if MCSP is NP-hard under polynomial-time reductions, then EXP  $\neq$  ZPP [26]. In a recent development, Hirahara [18] shows that if a certain approximation to MCSP is NP-hard, then NP  $\not\subseteq$  BPP implies that NP is difficult to compute even on average. In another recent development, several papers ([29], [27], [25]) study a "hardness magnification" phenomenon, whereby seemingly meager  $n \cdot \log^{\omega(1)} n$  circuit lower bounds on certain parameterizations of MCSP imply much stronger results such as NP  $\not\subseteq$  P/poly.<sup>2</sup>

MCSP is not hard for NP in limited settings. Murray and Williams [26] show MCSP is not NP-hard under a certain type of "local" reductions computable in  $\mathsf{TIME}(n^{0.49})$ . This is significant, since many well-known NP-complete problems are complete under local reductions computable in even logarithmic time. (A list of such problems is given in [26].) Also, under cryptographic assumptions, very weak approximations to MCSP are not NP-hard, even under P/poly reductions [7].

Many hardness results for MCSP also hold for approximate versions of MCSP. In various settings, the power of MCSP to distinguish between functions with circuits of size  $\theta$  and those requiring size  $\theta+1$  is not needed. Rather, in [3, 10, 4, 31, 28, 23], the reduction succeeds assuming only that reliable answers are given to queries on instances of the form  $(T, \theta)$ , where either the truth table T requires circuits of size  $\geq \theta = |T|^{.9}$  or T can be computed by circuits of size  $\leq |T|^{.01}$ .

This is an appropriate time to call attention to one such reduction to approximations to MCSP. Corollary 66 of [28] shows that, for every small  $\delta > 0$ , for every solution S to  $\mathsf{MCSP}[n^\delta, n^{.5}]^3$ , for every set  $A \in \mathsf{P/poly}$ , there is a c > 1 and a set A' that differs from A on at most  $(1/2 - 1/n^c)2^n$  of the strings of each length n, such that  $A' \leq_{\mathsf{tt}}^{\mathsf{AC}^0} S$  via a reduction<sup>4</sup> that makes only one query. (That is,  $A' \leq_{\mathsf{1-tt}}^{\mathsf{AC}^0} S$ .) Stated another way, any set in  $\mathsf{P/poly}$ 

<sup>&</sup>lt;sup>2</sup>The hardness magnification result we have stated here is from [25].

<sup>&</sup>lt;sup>3</sup>This promise problem is defined formally in Section 2.1.

<sup>&</sup>lt;sup>4</sup>Although Corollary 6 of [28] does not mention the number of queries, inspection of the proof shows that only one query is performed.

can be "approximated" with just one query to a weak approximation of MCSP. (Changing the solution S will yield a different set A'.)

There is no known many-one hardness result for MCSP, but one is known for a related problem. MKTP, the minimum time-bounded Kolmogorov complexity problem, is loosely the "program version" of MCSP. It is known [7] that MKTP is hard for DET under (non-uniform) NC<sup>0</sup> many-one reductions; it is conjectured that the same is true for MCSP. Time-bounded Kolmogorov complexity is polynomially-related to circuit complexity [3], so one natural way to extend the hardness result of [7] from MKTP to MCSP would be to stretch the very small gap given in the reduction of DET to MKTP.

### 1.1 Our Contributions, and Related Prior Work

We address the following questions based on prior work:

- 1. Can the non-hardness result of Murray and Williams [26] be extended to more powerful reductions? Both [26] and [8] conjecture that MCSP is not NP-complete under uniform AC<sup>0</sup> reductions.
- 2. Can the aforementioned conditional theorem of [7], establishing the non-NP-hardness of very weak approximations to MCSP under cryptographic assumptions, be improved, to show non-NP-hardness of MCSP for stronger approximations?
- 3. The worst-case to average case reduction given by [18] is conditional on the NP-hardness of a certain approximation to MCSP. Can we say anything about the NP-hardness of this problem in, say, the context of limited reductions?
- 4. Finally, can the result of [7], showing that MKTP is hard for DET under  $\leq_{\rm m}^{{\sf AC}^0}$  reductions, be extended, to hold for MCSP as well, by increasing the gap?

Our results give the following replies to these questions:

1. For superlinear approximations to MCSP, one can, in fact, give much stronger non-hardness results than [26], showing non-hardness even under non-uniform AC<sup>0</sup> many-one reductions and even limited types of AC<sup>0</sup> Turing reductions. To our knowledge, this is the first known non-hardness result for any variant of MCSP under non-uniform AC<sup>0</sup>

reductions. While  $AC^0$  reductions are provably less powerful than polynomial time reductions, most natural examples of NP-complete problems are easily seen to be complete under  $AC^0$  (and even  $NC^0$ !) reductions [11].

- 2. [7] shows that, if cryptographically-secure one-way functions exist, then  $\epsilon(n)$ -GapMCSP is not hard for NP under P/poly-Turing reductions<sup>5</sup> for some  $\epsilon(n) = n^{o(1)}$ . Our result gives a trade-off, where we reduce the gap dramatically but also weaken the type of reduction. In particular, our results imply that if one-way functions exist, then  $\epsilon(n)$ -GapMCSP is NP-intermediate under  $\leq_{\rm m}^{\rm AC^0}$  and  $\leq_{k-{\rm tt}}^{\rm AC^0}$  reductions, where  $\epsilon(n) = o(n)$ .
- 3. We show that the approximation to MCSP considered by [18] is actually not NP-hard under  $AC^0$  reductions.
- 4. Our work rules out one natural way to extend the MKTP hardness results to MCSP. One might have hoped that the reduction given by [7] could be extended to a larger gap and hence apply to MCSP (since MKTP and MCSP are polynomially related [3]). However, we show that this is impossible.

Our main theorem is an impossibility result in the setting of  $\epsilon(\theta)$ -GapMCSP, which is the promise version of MCSP with a multiplicative  $\epsilon(\theta)$  gap where  $\theta$  is the threshold.

**Theorem 1.** PARITY 
$$\nleq_{\mathbf{m}}^{\mathsf{AC}^0} \epsilon(\theta)$$
-GapMCSP where  $\epsilon(\theta) = o(\theta)$ .

We note that this is not the first work to describe non-hardness of approximation under  $\mathsf{AC}^0$  reductions. Arora [12] is credited by [1], with showing that no  $\mathsf{AC}^0$  reduction f can have the property that  $x \in \mathsf{PARITY}$  implies f(x) has a very large clique, and  $x \notin \mathsf{PARITY}$  implies f(x) has only very small cliques. (In Section 3, we present a similar result for Max-3-SAT, so that the reader can compare the techniques.) Our work differs from that of [12] in several respects. Arora shows that  $\mathsf{AC}^0$  reductions cannot prove very  $\mathit{strong}$  hardness of approximations for a problem where strong inapproximability results are already known. We show that  $\mathsf{AC}^0$  reductions cannot establish even very  $\mathit{weak}$  inapproximability results for MCSP. Also, our techniques allow us to move beyond  $\leq_{\mathrm{m}}^{\mathsf{AC}^0}$  reductions, to consider  $\mathsf{AC}^0$ -Turing reducibility.

<sup>&</sup>lt;sup>5</sup>The problem  $\epsilon$ -GapMCSP is defined somewhat differently in [7] than here. See Section 2. Thus the form of  $\epsilon(n)$  looks different here than in [7].

All of the theorems that we state in terms of MCSP hold also for MKTP, with identical proofs. For the sake of readability, we present the theorems and proofs only in terms of MCSP.

## 2 Preliminaries

We use  $\setminus$  to denote set difference. For a natural number n, we let [n] denote the set  $\{1, \ldots, n\}$ .

## 2.1 Defining MCSP

For any binary string T of length  $2^k$ , we define CC(T) to be the size of the smallest circuit (using only NOT gates and AND and OR gates of fan-in 2) that computes the function given by truth table T written in lexicographic order, where, for concreteness, circuit size is defined to be the number of AND and OR gates, although our arguments work for other reasonable notions of circuit size.

Throughout the paper, we use various approximate notions of the minimum circuit size problem, given as follows:

**Definition 2** (Gap MCSP). For any function  $\epsilon : \mathbb{N} \to \mathbb{N}$ , we define  $\epsilon(n)$ -GapMCSP to be the promise problem (Y, N) where

$$Y := \{ (T, \theta) \mid CC(T) \le \epsilon(\theta) \}, \text{ and }$$
  
 
$$N := \{ (T, \theta) \mid CC(T) > \theta \},$$

where  $\theta$  is written in binary.

Note that this definition differs in minor ways from the way that  $\epsilon$ -GapMCSP was defined in [7]. The definition presented here allows for finer distinctions than the definition that was used in [7].

Our results for non-hardness under  $\leq_{\mathrm{T}}^{\mathsf{AC}^0}$  reductions are best stated in terms of a restricted version of  $\epsilon$ -GapMCSP, where the thresholds are fixed, for inputs of a given size: This variant of MCSP has been studied previously in [26, 19]; an analogous problem defined in terms of KT-complexity is denoted  $R_{\mathsf{KT}}$  in [3].

**Definition 3** (Parameterized Gap MCSP). For any functions  $\ell, g : \mathbb{N} \to \mathbb{N}$  such that  $\ell(n) \leq g(n)$ , We define the language MCSP[ $\ell, g$ ] to be the promise problem (Y, N) where

$$Y := \{T \mid CC(T) \le \ell(|T|)\}, \text{ and } N := \{T \mid CC(T) > g(|T|)\}.$$

#### 2.2 Complexity classes and Reductions

We assume the reader is familiar with basic complexity classes such as P and NP. As we work extensively with non-uniform NC<sup>0</sup> and AC<sup>0</sup>, we refer to the text by Vollmer [33] for background on these circuit classes. Throughout this paper, unless otherwise explicitly mentioned, we refer to the non-uniform versions of these circuit classes.

Let  $\mathcal{C}$  be a class of circuits. For any languages A and B, we write  $A \leq_{\mathrm{m}}^{\mathcal{C}} B$  if there is a function f computed by a circuit family  $\{C_n\} \in \mathcal{C}$  such that  $f(x) \in B \iff x \in A$ . We write  $A \leq_{\mathrm{T}}^{\mathcal{C}} B$  if there is a circuit family in  $\mathcal{C}$  computing A with B-oracle gates. In particular, since we are primarily concerned with  $\mathcal{C} = \mathsf{AC}^0$ , we denote this as  $A \leq_{\mathrm{T}}^{\mathsf{AC}^0} B$ . We write  $A \leq_{\mathrm{tt}}^{\mathsf{AC}^0} B$  if there is an  $\mathsf{AC}^0$  circuit family computing A with B-oracle gates, where there is no directed path from any oracle gate to another, i.e. if the reduction is non-adaptive. If, furthermore, the non-adaptive reduction has the property that each of the oracle circuits contains at most k oracle gates, then we write  $A \leq_{k-\mathrm{tt}}^{\mathsf{AC}^0} B$ .

Let  $Y \subseteq \{0,1\}^*$  and  $N \subseteq \{0,1\}^*$  be disjoint. Then  $\Pi = (Y,N)$  is a promise problem. A language L is a solution to a promise problem  $\Pi = (Y,N)$  if  $Y \subseteq L$  and  $N \cap L = \emptyset$ . For two promise problems  $\Pi_1$  and  $\Pi_2$ , some type of reducibility r (many-one, truth table, or Turing), and a circuit class C, we say  $\Pi_1 \leq_r^C \Pi_2$  if there is a single family of oracle circuits  $\{C_n\}$  in C such that for every solution  $S_2$  of  $\Pi_2$ , there is a solution  $S_1$  of  $\Pi_1$  such that  $C_n$  computes an r-reduction from  $S_1$  to  $S_2$ .

#### 2.3 Boolean Strings and Functions

For an  $x \in \{0,1\}^n$  and a set of indices  $B \subseteq [n]$ , we let  $x^B$  denote the Boolean string obtained by flipping the *i*th bit of x for each  $i \in B$ .

A partial string (or restriction) is an element of  $\{0, 1, ?\}^*$ . Define the size of a partial string p to be the number of bits in which it is  $\{0, 1\}$ -valued. We say a partial string  $p \in \{0, 1, ?\}^n$  agrees with a binary string  $x \in \{0, 1\}^n$  if they agree on all  $\{0, 1\}$ -valued bits. If  $x \in \{0, 1\}^n$  is a binary string and  $B \subseteq [n]$ , then  $x|_B$  denotes the partial string given by replacing the jth bit of x with x for each x with x a partial string x a partial string x a partial string x a partial string x and x a partial string x a partial string x and x a partial string x and x and x are x and x and x are x and x are x and x and x are x are x and x are x are x and x are x and x are x are x and x are x are x and x are x and x are x are x and x are x are x and x are x and x are x and x are x are x and x are x and x are x are x and x are x and x are x are x and x are x are x and x are x are x are x and x are x are x and x are x and x are x and x are x are x are x and x are x are x and x are x are x and x are x and x are x are x and x are x are x and x are x and x are x are x and x are x and x are x are x are x and x are x are x and x are x and x are x are x and x are x and x are x are x and x are x are x are x are x are x and x are x are x and x are x and x are x are x are x are x and x are x are x and x are x and x are x and x are x are x and x are x are x and x are

A partial Boolean function on n variables is a function  $f: I \to \{0, 1\}$  where  $I \subseteq \{0, 1\}^n$ . For a promise problem  $\Pi = (Y, N)$  and  $n \in \mathbb{N}$ , we let  $\Pi|_n$  be the partial Boolean function that decides membership in Y on instances of length n which satisfy the promise. (In particular,  $\Pi|_n: I :=$ 

 $(Y \cup N) \cap \{0,1\}^n \to \{0,1\}.$ 

We will make use of two well-studied complexity measures on Boolean functions: block sensitivity and certificate complexity. We refer the reader to a detailed survey by Hatami, Kulkarni, and Pankratov [17] for background on these notions. For completeness, we provide the definitions of the two measures that we need. In our context, we will use these measures on partial Boolean functions. Let  $I \subseteq \{0,1\}^n$  and let  $f: I \to \{0,1\}$  be a partial Boolean function. For an input  $x \in I$ , define the block sensitivity of f at x, denoted bs(f,x), to be the maximum number of non-empty, disjoint sets  $B_1,\ldots,B_k$  such that  $x^{B_i} \in I$  and  $f(x) \neq f(x^{B_i})$  for all i. (Here, by " $f(y) \neq f(z)$ " we require that f is defined at both g and g.) Define the 0-block sensitivity of g to be g to be g to be the size of the smallest set g is an input g to be the size of the smallest set g is an input g to be that g is an input g to be g in that g is an input g to be the size of the smallest set g is an input g to be g in that g is an input g in the g in that g is an input g in the g in that g is an input g in the g in that g is an input g in the g in the g in that g is an input g in the g in that g is an input g in the g in that g is an input g in the g in

# 3 Prior Work

In this section, we present a result that is similar in spirit to a result reported by Arora in an unpublished manuscript [12]. There, it was shown that there is no  $\mathsf{AC}^0$ -computable function f with the property that  $x \in \mathsf{PARITY}$  implies f(x) has a very large clique, and  $x \notin \mathsf{PARITY}$  implies f(x) has only very small cliques. Here, in order to illustrate the techniques that were employed in [12], we observe that no  $\mathsf{AC}^0$  reduction can establish the known inapproximability of  $\mathsf{Max}$ -3-SAT [16].

Our results, like those of [12], rely on the following lemma, which says that it is possible to apply a restriction to a family of  $AC^0$  circuits and thereby obtain a family of  $NC^0$  circuits. This lemma is implicit in the earliest lower bound work on  $AC^0$  [2, 13], and was stated and proved in this form in [1].

**Lemma 4** (Lemma 7 in [1]). Let  $C_n$  be a family of n-input (multi-output)  $AC^0$  circuits. Then there exists an a > 0 such that for all  $n \in \mathbb{N}$  there exists a restriction of  $C_n$  to  $\Omega(n^{1/a})$  input variables that transforms  $C_n$  into a (multi-output)  $NC^0$  circuit.

Here, when we say that a restriction "transforms" a circuit into a NC<sup>0</sup> circuit, we mean the process whereby any OR gate that has a constant 1 feeding into it (say, from the restriction) can be replaced by a constant 1, and any AND gate that has a 0 feeding into it can be replaced by a constant 0, and this process can be repeated until no more simplification is possible.

**Proposition 5.** Let  $0 < \epsilon < 1$ . No AC<sup>0</sup> reduction f can have the property that  $x \in \mathsf{PARITY}$  implies  $f(x) \in \mathsf{3-SAT}$ , and  $x \notin \mathsf{PARITY}$  implies f(x) has at most an  $\epsilon$  fraction of the clauses satisfied.

Proof. By appealing to Lemma 4, we may assume that the function f is an  $\mathbb{NC}^0$  reduction. (A more careful argument, explaining how this assumption is justified, is provided in the proof of Theorem 1.) Let d be the constant, such that each output bit of f(x) depends on at most d bits of x, and let  $x \in \mathsf{PARITY}$  have length n. Let f(x) consist of m clauses, each encoded using  $c \log m$  bits for some constant c (which we can assume since the number of clauses is polynomially-related to the number of variables). Then since  $|f(x)| = cm \log m$ , and each output bit depends on at most d input bits, there is some  $i \leq n$  such that the i-th bit of x affects at most  $(dcm \log m)/n$  output bits. Flipping the i-th bit of x, to obtain a new string  $x' \notin \mathsf{PARITY}$  can affect at most  $(dcm \log m)/n$  clauses. Since  $f(x) \in \mathsf{3-SAT}$ , there is an assignment that satisfies at least  $m - (dcm \log m)/n$  clauses of f(x'). The theorem is proved, by observing that  $m - (dcm \log m)/n > \epsilon m$  for all large m.

This discussion of prior work is also the appropriate place to mention that a preliminary version of this article appeared in a conference proceedings [9]. Several proofs were omitted from the conference publication, due to space limitations, and they are presented in full here.

# 4 Non-Hardness Under NC<sup>0</sup> Reductions

In this section, we prove our main lemmas, showing that problems that are  $NC^0$ -reducible to  $\epsilon$ -GapMCSP have bounded 0-block sensitivity and also have sublinear 0-certificate complexity. Whenever we will have occasion to use these lemmas, it will be in situations when we are able to assume that the  $NC^0$  reduction is computing a function f satisfying the condition that there is a bound  $\gamma(n) > 0$  such that, for all n, there is a  $\theta \geq \gamma(n)$  such that, for all x of length n, f(x) is of the form  $(T(x), \theta)$ . (In particular, the threshold  $\theta$  is the same for all inputs of length n.) We will call such an  $NC^0$  reduction a  $\gamma$ -honest reduction.

**Lemma 6.** Let  $\epsilon(\theta) = o(\theta)$ , and let  $\Pi = (Y, N)$  be a promise problem, where  $\Pi \leq_{\mathrm{m}}^{\mathsf{NC}^0} \epsilon$ -GapMCSP via a  $\gamma$ -honest reduction f computed by an  $\mathsf{NC}^0$  circuit family  $C_n$  of depth  $\leq d$ , where  $\gamma(n) \geq \log \log n$ . Then there is an  $n_0$  (that depends only on  $\epsilon$  and d) such that for all  $n \geq n_0$ , if  $N|_n \neq \emptyset$ , then  $bs_0(\Pi|_n) < s$ , where s is a constant that depends only on d.

*Proof.* Let  $s = 2^{d+1} + 1$ . Since  $\epsilon(n) = o(n)$ , we can pick a constant  $r_0 > 4s$  such that  $\epsilon(r) < r/(2s)$  for all  $r \ge r_0$ .

Pick  $n_0 \ge 2^{2r_0}$ , and let  $n \ge n_0$ .

For the sake of contradiction, suppose  $bs_0(\Pi|_n) \geq s$ , and let  $x \in N \cap \{0,1\}^n$  be a 0-valued instance with  $bs(\Pi|_n,x) \geq s$ . Then we can find disjoint sets  $B_1, \ldots B_s \subseteq [n]$  such that  $\Pi|_n(x^{B_j}) = 1$  for all  $j \in [s]$ . (That is, each  $x^{B_j}$  is in Y.)

Let  $f(x) = (T, \theta)$ , and note that  $CC(T) > \theta \ge \gamma(n)$  (since f is  $\gamma$ -honest). Since  $x \in N$  and  $C_n$  is a reduction to  $\epsilon$ -GapMCSP, we know that any circuit that computes the function with truth table T has size at least  $\theta$ . For each  $j \in [s]$ , let  $T_j$  be the truth table produced by  $C_n$  on input  $x^{B_j}$ . Since  $x^{B_j} \in Y$ , we know that each  $T_j$  has a circuit  $D_j$  computing  $T_j$  of size at most  $\epsilon(\theta)$ . (Here, it is important that the same threshold  $\theta$  is used for all inputs of length n, by  $\gamma$ -honesty.)

We aim to build a "small" circuit computing T, which would contradict T having high complexity. Our circuit C for computing T works as follows: on input i, output the majority of  $D_1(i), \ldots, D_s(i)$ . The size of C is at most  $s \cdot \epsilon(\theta) + 2s$  (each  $D_j$  has size at most  $\epsilon(\theta)$ , and computing the majority of s bits can be done with a circuit of size 2s).

Now, we argue that this circuit correctly computes the *i*th bit of T for all i. Let i be arbitrary. Recall the *i*th bit of T is defined to be the *i*th output of  $C_n(x)$ . Since  $C_n$  is a depth d circuit of fan-in 2, the *i*th output of  $C_n$  depends on at most  $2^d$  input wires  $W \subseteq [m]$ . Hence, on any input y such that  $y|_W = x|_W$ , we have that the *i*th output of  $C_n(y)$  equals the *i*th output of  $C_n(x)$ . In particular, if B is disjoint from W, then the *i*th output of  $C_n(x^B)$  equals the *i*th output of  $C_n(x)$ . Since  $B_1, \ldots, B_s$  are disjoint and  $|W| \leq 2^d$ , it follows that at most  $2^d$  of the sets  $B_1, \ldots, B_s$  have a non-empty intersection with W. Hence, since  $s = 2^{d+1} + 1$ , the majority of the sets  $B_1, \ldots, B_s$  are disjoint with W, so the majority of the circuits  $D_1, \ldots, D_s$  when run on input i output the ith output of  $C_n(x)$ .

We thus have that  $CC(T) \leq s \cdot \epsilon(\theta) + 2s$ . But  $\theta > \gamma(n) \geq \log \log n$  (since the reduction f is  $\gamma$ -honest). By the choice of  $n_0$  we have  $\epsilon(\theta) < \theta/2s$  (since  $\theta > \log \log n \geq r_0$ ). Thus  $CC(T) \leq s \cdot \theta/2s + 2s = \theta/2 + 2s < \theta$  (since  $\theta > \log \log n > 4s$ ). This contradicts  $CC(T) > \theta$ .

The reader who is interested primarily in Theorem 1 (which shows that  $\mathsf{Gap}\ \mathsf{MCSP}$  is not  $\mathsf{NP}\text{-hard}$  under nonuniform  $\mathsf{AC}^0$  m-reductions) can skip ahead to Section 5. The rest of this section develops tools that are used in our results that deal with more powerful notions of reducibility.

**Lemma 7.** Let  $\epsilon(\theta) = o(\theta)$ , and let  $\Pi = (Y, N)$  be a promise problem, where  $\Pi \leq_{\mathrm{m}}^{\mathsf{NC}^0} \epsilon$ -GapMCSP via a  $\gamma$ -honest reduction f computed by an  $\mathsf{NC}^0$  circuit family  $C_n$  of depth  $\leq d$ , where  $\gamma(n) \geq \log \log n$ . Let  $k \geq 1$ . Then there is an  $n_0$  (that depends only on  $\epsilon, k$  and d) such that for all  $n \geq n_0$ , if  $N|_n \neq \emptyset$ , then  $c_0(\Pi|_n) \leq n/k$ .

*Proof.* Let  $p = 2^d$ , let  $p' = \binom{2pk+1}{p}$ , and let K be a constant that is specified later (and which depends only on k and d). Since  $\epsilon(\theta) = o(\theta)$ , we can pick a constant  $s_0$  such that  $\binom{p'}{2}\epsilon(s) + K < s$  for all  $s \ge s_0$ .

Pick  $n_0 \ge 2^{2^{s_0}}$ , and let  $n \ge n_0$ .

For contradiction, suppose  $c_0(\Pi|_n) > n/k$ . Let  $x \in N \cap \{0,1\}^n$  be a 0-valued instance with  $c_0(\Pi|_n, x) > n/k$ . Then, for all  $S \subseteq [n]$  with  $|S| \le n/k$ , there is an  $x_S$  such that  $x_S$  agrees with  $x|_S$  and such that  $\Pi|_n(x_S) = 1$ . (That is,  $x_S \in Y$ .)

Let  $(T, \theta)$  be the truth table produced by  $C_n$  on input x. Since  $x \in N$  and  $C_n$  is a reduction, we know that any circuit computing T has size at least  $\theta$ .

For each  $S \subseteq [n]$  with size at most n/k, let  $T_S$  be the truth table produced by  $C_n$  on input  $x_S$ . Since  $x_S \in Y$ , we know that  $T_S$  has a circuit  $D_S$  of size at most  $\epsilon(\theta)$ .

We aim to build a "small" circuit computing T, which would contradict that T has high complexity. Recall that  $p = 2^d$ , and that  $p' = \binom{2pk+1}{p}$ .

Claim 1. There exist sets  $S_1, \ldots S_{p'} \subseteq [n]$  such that

- $|S_i| \leq \frac{n}{2k}$  for all i, and
- for any set  $P \subseteq [n]$  with  $|P| \le p$ , we have that  $P \subseteq S_i$  for some i.

Proof. (Proof of Claim) Pick sets  $V_1, \ldots, V_{2pk+1} \subseteq [n]$  of size at most  $\frac{n}{2pk}$  whose union is [n]. Let  $\mathcal{V} = \{V_1, \ldots, V_{2pk+1}\}$ . Now let each of  $S_1, \ldots, S_{\binom{2pk+1}{p}}$  be the union of some p sets chosen from  $\mathcal{V}$ . Each  $S_i$  has size at most  $p\frac{n}{2pk} = \frac{n}{2k}$ . Let  $P \subseteq [n]$  be an arbitrary set of size p. Since  $\bigcup_{V \in \mathcal{V}} V = [n]$ , every element e of P lies within some  $V \in \mathcal{V}$ . Then P is contained in the union of some p sets from  $\mathcal{V}$ , so  $P \subseteq S_i$  for some i.

For each  $i \neq j \in [p']$ , let  $S_{i,j} = S_{j,i} = S_i \cup S_j$ . Note that  $|S_{i,j}| \leq n/k$ . Our circuit C for computing T works as follows. On input r, for each  $i \in [p']$ , see if  $D_{S_{i,1}}(r) = \cdots = D_{S_{i,p'}}(r)$ . If so, then output  $D_{S_{i,1}}(r)$ . The size of this circuit is at most  $\binom{p'}{2}\epsilon(\theta) + K$  (for some fixed constant K) since each of the  $\binom{p'}{2}$   $D_{S_{i,j}}$  circuits has size at most  $\epsilon(\theta)$  and the other "unanimity"

condition is a Boolean function on  $\binom{p'}{2}$  variables (of in fact linear size) and so can be computed with circuit of some size  $K = O(p')^2$  (that depends only on k and d).

Now, we argue that C on input r correctly computes the rth bit of T. Let  $r \in [m]$  be arbitrary. For convenience, given any input  $y \in \{0,1\}^n$  let  $C_n^r(y)$  denote the rth output of  $C_n(x)$ . Recall the rth bit of T is defined to be  $C_n^r(x)$ . We must show two things. First, that there exists an i such that  $D_{S_{i,1}}(r) = \cdots = D_{S_{i,p'}}(r)$  and second, that if for some i we have that  $D_{S_{i,1}}(r) = \cdots = D_{S_{i,p'}}(r)$ , then  $D_{S_{i,1}}(r) = C_n^r(x)$ .

Since  $C_n$  has depth d, the rth output of  $C_n$  can depend on at most  $2^d$  input wires  $W \subseteq [m]$ . Hence, on any input y such that  $y|_W = x|_W$ , we have that  $C_n^r(y) = C_n^r(x)$ . Since  $p = 2^d$ , by the claim, there exists some  $S_{i^*}$  such that  $W \subseteq S_{i^*}$ . Therefore, for all j we have that  $x_{S_{i^*,j}}|_W = x|_W$ , so

 $D_{S_{i^{\star},j}}(r) \stackrel{\text{def}}{=} C_n^r(x_{S_{i^{\star},j}}) = C_n^r(x).$  This implies both things we must show. First, we know that  $D_{S_{i^{\star},1}}(r) = \cdots = D_{S_{i^{\star},p'}}(r)$  since they each equal  $C_n^r(x)$ . Second, if for some i, we have that  $D_{S_{i,1}}(r) = \cdots = D_{S_{i,p'}}(r)$ , then we also have that  $D_{S_{i,1}}(r) = D_{S_{i,i^{\star}}}(r) = C_n^r(x)$ .

Thus we have that T can be computed by a circuit of size at most  $\binom{p'}{2}\epsilon(\theta)+K$ , which is less than  $\theta$ , since  $\theta \geq \log\log n \geq s_0$ . This contradicts that  $CC(T)>\theta$ .

Next, we note that one can improve the bounds given by Lemma 7 assuming a larger gap.

**Lemma 8.** Let  $\epsilon(\theta) < \theta^{\alpha}$ , and let  $\Pi = (Y, N)$  be a promise problem, where  $\Pi \leq_{\mathbf{n}}^{\mathsf{NC}^0} \epsilon$ -GapMCSP via a  $\gamma$ -honest reduction f computed by an  $\mathsf{NC}^0$  circuit family  $C_n$  of depth  $\leq d$ , where  $\gamma(n) \geq n^{\beta}$ . Then for all  $\delta$  such that  $\delta_0 = \beta(1-\alpha)/2^{d+1} > \delta > 0$  there is an  $n_0$  such that for all  $n \geq n_0$ , if  $N|_n \neq \emptyset$ , then  $c_0(\Pi|_n) \leq n^{1-\delta}$ .

Proof. Let  $p=2^d$ . Suppose for contradiction that for some  $\delta>0$  with  $\delta<\delta_0=\beta(1-\alpha)/2p$  we have  $c_0(\Pi|_n)>n^{1-\delta}$  infinitely often. We can follow the same argument (and notation) as above, except we have to be more careful since  $n/c_0(\Pi|_n)$  is no longer a constant, and hence  $p'=\binom{2pn/c_0(\Pi|_n)+1}{p}\leq \binom{2pn^\delta+1}{p}=O(n^{p\delta})$  is no longer constant. Since the unanimity condition can be implemented by a circuit of size linear in  $\binom{p'}{2}$ , we can construct a circuit computing truth table T of size

$$\epsilon(\theta) \cdot c_1 p'^2 = \epsilon(\theta) \cdot c_1 \binom{2pn^{\delta} + 1}{p}^2 \le c_2 \epsilon(\theta) n^{2p\delta}$$

infinitely often for some positive constants  $c_1, c_2$ . By  $\gamma$ -honesty, we have  $\theta \geq \gamma(n) \geq n^{\beta}$ . This implies that we can construct a circuit computing T of size

$$c_2 \epsilon(\theta) n^{2p\delta} \le c_2 \epsilon(\theta) (\theta^{1/\beta})^{2p\delta} < c_2 \theta^{\alpha} \theta^{2p\delta/\beta} < \theta$$

infinitely often. This is a contradiction since T is a truth table with circuit complexity  $\geq \theta$ .

Next, we present a variant of Lemma 8, but restricted to the parameterized version of MCSP. This variant is useful in extending our non-hardness results to  $\leq_{\Lambda}^{\Lambda C^0}$  reductions that make  $n^{o(1)}$  queries.

**Lemma 9.** Let  $\Pi = (Y, N)$  be a promise problem. If  $\Pi \leq_{\mathrm{m}}^{\mathsf{NC}^0} \mathsf{MCSP}[\ell, g]$  with  $\ell(m) = o(g(m)/m^{\delta})$  for some  $\delta > 0$ , then  $c_0(\Pi|_n) \leq n^{\epsilon}$  for some  $\epsilon < 1$  for all but finitely many n where  $N|_n \neq \emptyset$ , where  $\epsilon$  depends only on the depth of the  $\mathsf{NC}^0$  circuit family and  $\delta$ .

*Proof.* Suppose for contradiction that for all  $\epsilon < 1$  we have  $c_0(\Pi|_n) > n^{\epsilon}$  infinitely often. Once again, we follow the same argument (and notation) as above. We can construct a circuit computing truth table T of size

$$\ell(m) \cdot c_1 p'^2 \le \ell(m) \cdot c_1 \binom{2pn/c_0(\Pi|_n) + 1}{p}^2 \le \ell(m)c_1 \binom{2pn^{1-\epsilon} + 1}{p}^2 \le c_2 \ell(m)n^{2p(1-\epsilon)},$$

infinitely often for some positive constants  $c_1, c_2$ . (Here, m denotes the length of the truth table T.) Note that since  $c_0(\Pi|_n) > n^{\epsilon}$ , we know  $\Pi|_n$  depends on  $\geq n^{\epsilon}$  input bits. Since the circuit has depth at most d and gates of fan-in 2, we must have  $m \geq n^{\epsilon}/2^d$ . This implies that we can construct a circuit computing T of size

$$c_2\ell(m)(n^{\epsilon})^{\frac{2p(1-\epsilon)}{\epsilon}} \le c_3\ell(m)m^{\frac{2p(1-\epsilon)}{\epsilon}},$$

infinitely often for some positive constant  $c_3$ . Setting  $\epsilon = \frac{2p}{2p+\delta}$ , we have that T can be computed by a circuit of size  $\leq c_3\ell(m) \cdot m^{\delta}$  infinitely often, which is a contradiction since T is a truth table with circuit complexity  $\geq g(m) = \omega(\ell(m) \cdot m^{\delta})$ .

# 5 Non-Hardness Under Many-One AC<sup>0</sup> Reductions

In this section, we use the tools of the preceding section to show that the problem of approximating circuit size is not hard for any class containing PARITY under  $\leq_{\rm m}^{\rm AC^0}$  reductions. We recall Theorem 1:

**Theorem 1.** PARITY  $\leq_m^{AC^0} \epsilon$ -GapMCSP where  $\epsilon(n) = o(n)$ .

Proof. Suppose not. Then there is a family of  $AC^0$  circuits  $C_n$  that many-one reduces PARITY to  $\epsilon$ -GapMCSP. By Lemma 4, there is an a such that we can transform each  $C_n$  into an  $NC^0$  circuit  $D_m$  on  $m = \Omega(n^{1/a})$  variables, computing a reduction f from either PARITY or  $\neg PARITY$  (depending on the parity of the restriction) to  $\epsilon$ -GapMCSP. For each input x of length n, f(x) is of the form  $(T(x), \theta(x))$ . Since there are only  $O(\log n)$  output gates in the  $\theta(x)$  field, and each output gate depends on only O(1) input variables, all of the output gates for  $\theta(x)$  can be fixed by setting only  $O(\log n)$  input variables. Furthermore, we claim that there is some setting of these  $O(\log n)$  input variables, such that the resulting value of  $\theta$  is greater than  $\log n/\log\log n$ . If this were not the case, then the  $\leq_{\rm m}^{\rm AC^0}$  reduction of PARITY (or  $\neg PARITY$ ) on  $m = \Omega(n^{1/a})$  variables to  $\epsilon$ -GapMCSP has the property that  $\theta(x)$  is always less than  $\log n/\log\log n$ . But, as in the proof of Theorem 1.3 of [26], instances of MCSP where  $\theta$  is  $O(\log n/\log\log n)$  can be solved with a DNF circuit of polynomial size. Thus this would give rise to  $AC^0$  circuits for PARITY, contradicting the well-known circuit lower bounds of [2, 13].

Summarizing up to this point: The circuits  $D_m$  with  $O(\log n)$  additional variables set (fixing the value of  $\theta$ ) yields a family on  $m' = m - O(\log n) = \Omega(n^{1/(a+1)})$  variables, where each circuit  $D_{m'}$  reduces either PARITY or ¬PARITY to  $\epsilon$ -GapMCSP, where furthermore this reduction satisfies the hypotheses of Lemmas 6 and 7.

But then the conclusions of Lemmas 6 and 7 contradict the fact that both PARITY and  $\neg PARITY$  on m' variables have 0-certificate complexity and 0-block-sensitivity m'.

# 6 Non-Hardness Under Limited Turing AC<sup>0</sup> Reductions

With some work, we can extend our non-hardness results beyond many-one reductions to some limited Turing reductions.

In our proofs that deal with  $AC^0$ -Turing reductions, we will need to replace some oracle gates with "equivalent" hardware — where this hardware will provide answers that are consistent with *some* solution to the promise problem  $\epsilon$ -GapMCSP, but might not be consistent with the particular solution that is provided as an oracle. In order to ensure that this doesn't cause any problems, we introduce the notion of a "sturdy"  $AC^0$ -Turing reduction:

**Definition 10.** Let  $\Pi_1 = (Y_1, N_1)$  and  $\Pi_2 = (Y_2, N_2)$  be promise problems. A family  $\{C_n\}$  of  $\mathsf{AC}^0$ -oracle circuits is a sturdy  $\leq^{\mathsf{AC}^0}_{\mathsf{T}}$  reduction from  $\Pi_1$  to  $\Pi_2$  if, for every pair of solutions S, S' to  $\Pi_2$ , every oracle gate G in  $C_n$ , and every  $x \in Y_1 \cup N_1$ , there is a solution S'' such that  $C_n^S(x) = C_n^{S''}(x) = C_n^S[G \to S'](x)$ , where the notation  $C_n^S[G \to S']$  refers to the circuit  $C_n$  with oracle S, but where the oracle gate G answers queries according to the solution S' instead of S.

**Lemma 11.** Let  $\Pi$  be any promise problem. If  $\Pi \leq_{\mathrm{tt}}^{\mathsf{AC}^0} \epsilon(n)$ -GapMCSP via a reduction of depth d, then  $\Pi \leq_{\mathrm{tt}}^{\mathsf{AC}^0} \epsilon(n)$ -GapMCSP via a sturdy reduction of depth 5d with the same number of oracle gates. If  $\Pi \leq_{\mathrm{T}}^{\mathsf{AC}^0} \epsilon(n)$ -GapMCSP via a reduction of depth d, then  $\Pi \leq_{\mathrm{T}}^{\mathsf{AC}^0} \epsilon(n)$ -GapMCSP via a sturdy reduction of depth 5d with the same number of oracle gates.

*Proof.* Briefly: We modify  $C_n$ , so that each oracle query is checked against queries that were asked "earlier" in the computation, and the computation uses only the oracle answer from the first time a query was asked. Since each query is given an answer that is consistent with *some* solution, the new circuit gives the same answers as a new solution (which we denote as S''). Since  $C_n$  is a reduction, we get the same answer when using S or S''.

In more detail: Label the oracle gates  $G_1, \ldots, G_k$  of  $C_n$  in topological order so that there is no directed path from  $G_i$  to  $G_j$  for all i > j (and for a truth-table reduction, any ordering suffices). Let  $q_i$  denote the query asked by  $G_i$ . Let  $C'_n$  be the circuit where we replace any wire that leaves  $G_i$  by a wire connected to the following subfunction:

$$G_i(x) \land \forall j < i(q_i \neq q_j)$$
 or 
$$\exists j < i(q_i = q_j \land \forall k < j(q_k \neq q_j) \land G_j(q_j))$$

The reader can verify that this additional circuitry can be implemented in depth five, and thus  $C'_n$  has depth at most 5d. Furthermore, this hardware does not add any oracle gates or directed paths between oracle gates, so the number of oracle gates used is unchanged and truth-table reductions remain truth-table reductions.

Now let S and S' be any two solutions to  $\epsilon(n)$ -GapMCSP. Consider any input x of length n that satisfies the promise of  $\Pi=(Y,N)$ . (That is,  $x\in Y\cup N$ .) Thus  $C_n^S(x)=C_n^{S'}(x)$ . Now consider the operation of  $C_n'(x)$  where some oracle gate  $G_i$  answers queries according to S', rather than S. By construction, the behavior of this computation  $C_n'^S[G_i\to S']$  is the same

as that of  $C_n^{S''}(x)$ , where

$$S''(q(x)) := \begin{cases} S(q(x)) & \text{if } q(x) \neq q_i(x), \text{ or if } q_i(x) = q_j(x) \text{ for some } j < i, \\ S'(q(x)) & \text{otherwise.} \end{cases}$$

S'' is also a solution to  $\epsilon$ -GapMCSP, since it agrees with either S or S' on each query, and both S and S' agree on all queries that satisfy the promise. Thus  ${C'}_n^S[G_i \to S'](x) = {C}_n^{S''}(x) = {C}_n^S(x) = {C}_n^S(x)$ , since  $C_n$  is a reduction. Also,  ${C'}_n^{S''}(x) = {C}_n^{S''}(x)$  and  ${C'}_n^S(x) = {C}_n^S(x)$ , since each oracle gate of  ${C'}_n$  answers each query the same way that  $C_n$  does, if the same oracle is provided to each gate. Thus, we have that  ${C'}_n^S(x) = {C'}_n^{S''}(x) = {C'}_n^S[G_i \to S'](x)$ . This establishes that  ${C'}_n$  is computing a sturdy reduction.

**Theorem 12.** Let  $k \geq 1$ , and let  $\epsilon(n) = o(n)$ . Then PARITY  $\nleq_{k-\text{tt}}^{\mathsf{AC}^0}$   $\epsilon\text{-}\mathsf{GapMCSP}$ .

*Proof.* We show that, for all  $k \geq 1$ , if PARITY  $\leq_{k=\mathrm{tt}}^{\mathsf{AC}^0} \epsilon$ -GapMCSP, then PARITY  $\leq_{(k-1)-\mathrm{tt}}^{\mathsf{AC}^0} \epsilon$ -GapMCSP. This suffices, since a 0-truth-table reduction is simply an  $\mathsf{AC}^0$  circuit computing PARITY, which cannot exist.

Given the oracle circuit family  $C_n$ , (where by Lemma 11 we may assume that the  $\leq_{k-\text{tt}}^{\mathsf{AC}^0}$  reduction is sturdy), let  $D_n$  be the subcircuit consisting of those gates that are on a path from an input variable to any oracle gate.  $D_n$  is simply an  $\mathsf{AC}^0$  circuit on n variables, and thus by Lemma 4, there is an a such that we can transform each  $D_n$  into an  $\mathsf{NC}^0$  circuit  $E_{m(n)}$  on  $m(n) = \Omega(n^{1/a})$  variables. Replacing  $D_n$  by  $E_{m(n)}$  in  $C_n$  yields a k-tt reduction  $F_{m(n)}$  from PARITY or ¬PARITY on m(n) variables to  $\epsilon$ -GapMCSP. (If  $F_{m(n)}$  is a reduction from ¬PARITY, then modify  $F_{m(n)}$  by negating the output gate, so that each  $F_{m(n)}$  is a reduction from PARITY on m(n) variables to  $\epsilon$ -GapMCSP.) Note that we can obtain a family of polynomial-size circuits on n variables by starting with  $F_{m(n)^{2a}}$  (which has more than n input variables) and setting some of the variables to 0. Thus, without any loss of generality, we may assume that our circuit family  $C_n$  has the property that the subcircuit  $D_n$  consisting of the gates on a path from an input gate to an oracle gate consists of  $\mathsf{NC}^0$  circuitry.

For each n, select the first oracle gate  $G_1$  (in some order). Consider the circuit family  $B_n$  consisting of all of the gates that are on a path from any input to  $G_1$ . Note that  $B_n$  is an  $NC^0$  circuit family computing some function f, where f(x) is of the form  $(T(x), \theta(x))$ . If it is possible to set some of the input variables of  $B_n$  so that the output gates for  $\theta(x)$  take on a value  $\theta \ge \log n/\log\log n$ , do so. Note that this leaves  $m = n - O(\log n)$  variables

unset. (If it is not possible to do so, then (as in the proof of Theorem 1),  $G_1$  can be replaced in  $C_n$  by a polynomial-sized DNF circuit, thereby yielding a (sturdy) (k-1)-tt reduction, as desired.) Call  $C'_m$  and  $B'_m$  the circuits that result by restricting the  $O(\log n)$  input variables of  $C_n$  and  $B_n$ , respectively.

We now aim to find a restriction of the inputs and a solution to  $\epsilon$ -GapMCSP such that the output of  $G_1$  is constant. Define  $\Pi = (Y, N)$  to be the promise problem where for all x we put  $x \in Y$  if and only if  $\mathrm{CC}(T(x)) \leq \epsilon(\theta)$  and  $x \in N$  if and only if  $\mathrm{CC}(T(x)) > \theta$  where  $B'_m(x) = (T(x), \theta)$ . Observe that  $B'_m$  is a log n-honest  $\mathrm{NC}^0$  reduction of  $\Pi$  to  $\epsilon$ -GapMCSP.

There are two cases, depending on whether  $N=\emptyset$  or not. If  $N=\emptyset$ , then  $S'=\{(T,\theta): \mathrm{CC}(T)\leq \epsilon(\theta)\}$  is a solution to  $\epsilon$ -GapMCSP such that every query to  $G_1$  is answered affirmatively. By the sturdiness of the reduction,  $G_1$  can be replaced by a constant 1, transforming  $C'_m$  into a (k-1)-tt reduction.

If  $N \neq \emptyset$ , then by Lemma 7, for all large m  $c_0(\Pi|_m) \leq m/(k+1)$ . That is, there is a way to set some  $r \leq m/(k+1)$  input variables, obtaining restriction  $\rho$ , and thereby obtain a circuit  $B''_{m-r} = B'_m|_{\rho}$  on m-r variables, such that for any string z of length m-r,  $\mathrm{CC}(T_{m-r}(z)) > \epsilon(\theta)$  where  $B''_{m-r}(z) = (T_{m-r}(z), \theta)$ . That is, every query to  $G_1$  is answered negatively in  $C'_m|_{\rho}$ , and hence  $G_1$  can be replaced by a constant 0, transforming  $C'_m|_{\rho}$  into a (k-1)-tt reduction from PARITY to  $\epsilon$ -GapMCSP on  $m-r=\Omega(n)$  variables in this case.

In both cases, we obtain a (k-1)-tt reduction from PARITY to  $\epsilon$ -GapMCSP, as desired.

With a larger gap, we can rule out nonadaptive reductions that use  $n^{o(1)}$  queries.

**Theorem 13.** Let  $\epsilon(n) < n^{\alpha}$  for some  $1 > \alpha > 0$ . Then for any circuit family  $\{C_n\}$  computing an  $\leq_{\mathrm{tt}}^{\mathsf{AC}^0}$  reduction of PARITY to  $\epsilon$ -GapMCSP, there is a  $\delta > 0$  such that, for all large n,  $C_n$  makes at least  $n^{\delta}$  queries.

Proof. Let  $\{C_n\}$  be a circuit family computing an  $\leq_{\mathrm{tt}}^{\mathsf{AC}^0}$  reduction of PARITY to  $\epsilon$ -GapMCSP. By Lemma 11 we may assume that each  $C_n$  is sturdy. As in the proof of the preceding theorem, we assume without loss of generality that  $C_n$  has the property that the subcircuit  $D_n$  consisting of those gates that lie on paths from input gates to oracle gates consists of  $\mathsf{NC}^0$  circuitry of depth d. (We will assume without loss of generality that, if the gates in  $D_n$  are removed from  $C_n$ , the depth of the circuit that remains is also at most d. Otherwise, let d be the maximum of these two constants.)

We will show that, for all large n,  $C_n$  contains at least  $n^{\delta}$  oracle gates  $G_1, G_2, \ldots, G_t$ , where  $\delta$  is chosen to be less than  $(1 - \alpha)/12d2^{d+1}$ . For the

sake of a contradiction, assume that  $t < n^{\delta}$ .

Here is a high-level overview of the rest of the proof: As in the proof of the preceding theorem, we construct a sequence of restrictions (one for each oracle gate), so that when the input bits of  $C_n$  are set according to the restrictions, each oracle gate either has a very small threshold  $\theta$ , or else it can be replaced by a constant. In this way, we transform  $C_n$  into a circuit on  $m \geq n/2$  input bits where each oracle gate  $G_i$  has a threshold  $\theta_i < n^{1/3d}/\log n$ . Replacing each such oracle gate by a DNF of size  $2^{O(n^{1/3d})}$  (as in the proof of the preceding theorem) results in an AC<sup>0</sup> circuit of depth at most d+1 computing PARITY, in contradiction to the lower bound of [15]. Details follow.

Our argument proceeds in t stages, where oracle gate  $G_i$  is considered in stage i. At the start of stage i we have a partial restriction  $\rho_{i-1}$  that has at most  $(i-1)n^{1-2\delta}$  bits set. Here is a detailed description of stage i:

Consider the circuit family  $B_n$  consisting of all of the gates that are on a path from any input to  $G_i$ . Note that  $B_n$  is an  $NC^0$  circuit family computing some function  $f_i$ , where  $f_i(x)$  is of the form  $(T_i(x), \theta_i(x))$ . If for all x that agree with  $\rho_{i-1}$ ,  $\theta_i(x) < n^{1/(3d)}/\log(n)$ , then stage i is done; set  $\rho_i = \rho_{i-1}$  and go on to the next stage. Otherwise, there is a way to set an additional  $O(\log n)$  additional variables, thereby extending  $\rho_{i-1}$  to obtain a new restriction  $\rho'_i$ , so that for all x which agree with  $\rho'_i$ ,  $\theta_i(x)$  takes on a constant value  $\theta_i \geq n^{1/(3d)}/\log n \geq n^{1/(4d)}$ .

We now aim to find a restriction of the inputs and a solution to  $\epsilon$ -GapMCSP such that the output of  $G_i$  is constant. Define  $\Pi_i = (Y_i, N_i)$  to be the promise problem where for all x that agree with  $\rho_i'$  we put  $x \in Y_i$  if and only if  $\mathrm{CC}(T_i(x)) \leq \epsilon(\theta_i)$  and  $x \in N_i$  if and only if  $\mathrm{CC}(T_i(x)) > \theta_i$  where  $B_n(x) = (T_i(x), \theta_i)$ . Observe that  $B_n$  is a  $n^{1/(4d)}$ -honest  $\mathrm{NC}^0$  reduction of  $\Pi_i$  to  $\epsilon$ -GapMCSP.

There are two cases, depending on whether  $N_i = \emptyset$  or not. If  $N_i = \emptyset$ , then  $S = \{(T, \theta) : CC(T) \leq \theta\}$  is a solution to  $\epsilon$ -GapMCSP such that every query to  $G_i$  is answered affirmatively. By the sturdiness of the reduction, the output of  $G_i$  can be replaced by the constant 1, and we let  $\rho_i = \rho'_i$ .

If  $N_i \neq \emptyset$ , then by Lemma 8, for all large n,  $c_0(\Pi_i|_{\rho_i'}) \leq n^{1-3\delta}$ . (The conditions of Lemma 8 are satisfied, since  $(1/4d)(1-\alpha)/2^{d+1} > 3\delta$ .) That is, there is a way to set at most  $n^{1-3\delta}$  additional variables, thereby extending  $\rho_i'$  to obtain a new restriction  $\rho_i$ , such that for any string x of length n that agrees with  $\rho_i$ ,  $CC(T_i(x)) > \epsilon(\theta_i)$ . Therefore,  $S = \{(T, \theta) : CC(T) \leq \epsilon(\theta)\}$  is a solution to  $\epsilon$ -GapMCSP such that every query to  $G_i$  is answered negatively. Hence, by the sturdiness of the reduction, gate  $G_i$  can be replaced by a

constant 0.

This completes stage i. Note that, in obtaining  $\rho_i$  from  $\rho_{i-1}$  we set an additional  $O(\log n) + n^{1-3\delta} < n^{1-2\delta}$  variables.

Since  $t < n^{\delta}$ , we have that  $\rho_t$  has  $m \ge n - tn^{1-2\delta} > n - n^{\delta}n^{1-2\delta} = n - n^{1-\delta} > n/2$  unset variables. Let  $C''_m$  be the circuit  $C_n|_{\rho_t}$ . Each oracle gate in  $C''_m$  has the property that the threshold that is computed is always no more than  $n^{1/3d}$ . Since the reduction is sturdy, the circuit still behaves correctly if each oracle gate is replaced by a circuit that computes MCSP exactly, and (as in the proof of Theorem 1.3 of [26]), instances of MCSP where  $\theta$  is bounded by  $n^{1/3d}/\log n$  can be computed by a DNF of size  $2^{O(n^{1/3d})}$ . Replacing each oracle gate by such a DNF yields a circuit of depth at most d+1, of size  $2^{O(n^{1/3d})}$ , computing PARITY, thereby violating the lower bound established in [15].

If we consider the parameterized version of MCSP, rather than  $\epsilon$ -GapMCSP, we obtain non-hardness even under  $\leq_T^{AC^0}$  reductions.

**Theorem 14.** Let  $\ell(m) = o(g(m)/m^{\delta})$  for some  $1 > \delta > 0$ . Then for any circuit family  $\{C_n\}$  computing an  $\leq_{\mathrm{T}}^{\mathsf{AC}^0}$  reduction of PARITY to  $\mathsf{MCSP}[\ell, g]$ , there is an  $\epsilon > 0$  such that, for all large n,  $C_n$  makes at least  $n^{\epsilon}$  queries.

*Proof.* Define the *oracle depth* of a gate G to be the largest number of oracle gates on any directed path ending with G.

Let  $\{C_n\}$  be a circuit family computing an  $\leq_{\mathrm{T}}^{\mathsf{AC}^0}$  reduction of PARITY to  $\mathsf{MCSP}[\ell, g]$ . As above, we may assume that each  $C_n$  is sturdy, and that the subcircuit  $D_n$  consisting of those gates at oracle depth 1 consists of  $\mathsf{NC}^0$  circuitry of depth at most d. Let k be the maximum oracle depth of any gate in  $\{C_n\}$ .

Here is a high-level overview of the rest of the proof: Similar to the proof of the preceding theorem, we construct a sequence of t restrictions  $\rho_1, \ldots, \rho_t$ , so that in  $C_n|_{\rho_i}$  the first i gates  $G_1, \ldots, G_i$  can be replaced a constant. In this way, we transform  $C_n$  into a circuit on  $n' \geq n/2$  input bits of oracle depth k-1.

We will first show that there is a value  $\epsilon > 0$  (specified later) such that if  $C_n$  does not have at least  $n^{\epsilon}$  gates at oracle depth 1, then  $C_n$  can be replaced by an  $\leq_{\mathrm{T}}^{\mathsf{AC}^0}$  reduction of oracle depth k-1, by eliminating all of the oracle gates  $G_1, \ldots, G_t$  at oracle depth 1.

Our argument proceeds in t stages, where oracle gate  $G_i$  is considered in stage i. At the start of stage i we have a partial restriction  $\rho_{i-1}$  that has at most  $(i-1)n^{1-2\epsilon}$  bits set. Here is a detailed description of stage i:

Consider the circuit family  $B_n$  consisting of all of the gates that are on a path from any input to  $G_i$ . Note that  $B_n$  is an  $NC^0$  circuit family computing some function  $f_i(x) = T_i(x)$ . Let  $m = |T_i(x)|$ . Also, although  $B_n$  sits inside of  $C_n$  (which is computing PARITY), the function  $f_i$  might not have any obvious connection to PARITY. By the end of the next paragraph, we will have identified some relevant properties of  $f_i$ .

We now aim to find a restriction of the inputs and a solution to  $\mathsf{MCSP}[\ell,g]$  for which the output of  $G_i$  is constant. Define  $\Pi_i = (Y_i, N_i)$  to be the promise problem where for all x that agree with  $\rho_{i-1}$  we put  $x \in Y_i$  if and only if  $\mathrm{CC}(T_i(x)) \leq \ell(m)$  and  $x \in N_i$  if and only if  $\mathrm{CC}(T_i(x)) > g(m)$ . Observe that by construction of  $\Pi_i$ ,  $B_n$  is an  $\mathsf{NC}^0$  reduction of  $\Pi_i$  to  $\epsilon$ -GapMCSP.

There are two cases, depending on whether  $N = \emptyset$  or not. If  $N = \emptyset$ , then  $S = \{T : CC(T) \leq g(|T|)\}$  is a solution to  $\mathsf{MCSP}[\ell, g]$  such that every query to  $G_i$  is answered affirmatively. By the sturdiness of the reduction, the output of  $G_i$  can be replaced by the constant 1, and we let  $\rho_i = \rho_{i-1}$ .

If  $N \neq \emptyset$ , then, by Lemma 9, for all large n,  $c_0(\Pi_i|_{\rho_{i-1}}) \leq n^{\epsilon'}$  for some  $\epsilon' < 1$  that depends only on d and  $\delta$ . That is, there is a way to set at most  $n^{\epsilon'}$  additional variables, thereby extending  $\rho_{i-1}$  to obtain a new restriction  $\rho_i$ , such that for any string x of length n that agrees with  $\rho_i$ ,  $CC(T_i(x)) > \ell(m)$ . Thus,  $S = \{T : CC(T) \leq \ell(m)\}$  is a solution to  $MCSP[\ell, g]$  such that every query to  $G_i$  is answered negatively. Therefore, by the sturdiness of the reduction, gate  $G_i$  can be replaced by a constant 0.

This completes stage i. Note that, in obtaining  $\rho_i$  from  $\rho_{i-1}$  we set an additional  $n^{\epsilon'}$  variables.

It is now time to set the constant  $\epsilon$  to be  $1 - (\epsilon'/2)$ .

Since  $t < n^{\epsilon}$ , we have that  $\rho_t$  has  $r \ge n - tn^{\epsilon'} = n - n^{1-(\epsilon'/2)}n^{\epsilon'} = n - n^{1-(\epsilon'/2)} > n/2$  unset variables.

A minor complication arises when we want to repeat this argument inductively to reduce the oracle depth to k-2 and so on. Namely, the constant  $\epsilon'$  depends on the depth d of the  $NC^0$  circuitry that feeds into the oracle gates at the bottom level of  $C_n$ .  $C_n|_{\rho_t}$  has oracle depth k-1, as desired, but it now has  $AC^0$  circuitry feeding into the lowest level of oracle gates, and when we appeal to Lemma 4 to apply a random restriction to convert that  $AC^0$  circuitry to  $NC^0$  circuitry, the depth of the  $NC^0$  circuitry increases to a depth that we can denote  $d_2$ .

However, this problem is resolved by observing that the choice of  $\epsilon'$  in Lemma 9 is monotone in the depth d. Thus, if we carry out the argument above, but pick  $\epsilon'$  using the parameter  $d_2$  instead of d when we appeal to Lemma 9, and then repeat the argument to reduce the oracle depth to k-2, the parameters still work out. If we let  $d_3$  be the depth of the  $NC^0$  circuitry

that results by starting with  $C_n$  with depth-d NC<sup>0</sup> circuitry at the bottom, eliminating lowest level of oracle gates and applying a random restriction to obtain a circuit family of oracle depth k-1 with NC<sup>0</sup> circuitry of depth  $d_2$  at the bottom, and then repeating the process to obtain a circuit family of oracle depth k-2 with NC<sup>0</sup> circuitry of depth  $d_3$  at the bottom, then the argument above is sufficient to obtain a circuit family of depth k-3, etc.

Thus, there is a choice of  $\epsilon'$  that suffices to convert an arbitrary  $\leq_{\mathrm{T}}^{\mathsf{AC}^0}$  reduction of oracle depth k (with fewer than  $n^{\epsilon}$  oracle gates) to an  $\mathsf{AC}^0$  circuit computing parity on  $n^{\Omega(1)}$  input bits, thereby obtaining the desired contradiction.

# 7 Open Questions

There remain several open questions. The true complexity of MCSP remains a mystery. We have made progress in understanding the hardness of an approximation to MCSP, but how far can Theorem 1 be extended? Can we prove non-hardness under general truth-table and Turing reductions? Can we reduce the gap in the theorem to some constant factor approximations? Does the impossibility result hold when  $AC^0$  is replaced with, say,  $AC^0[2]$  many-one reductions? Is MCSP hard for DET under  $\leq_m^{AC^0}$  reductions? (Recall that the related problem MKTP is hard for DET under such reductions [7].)

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## References

- [1] Manindra Agrawal, Eric Allender, and Steven Rudich. Reductions in circuit complexity: An isomorphism theorem and a gap theorem. *J. Comput. Syst. Sci.*, 57(2):127–143, October 1998.
- [2] Miklos Ajtai.  $\Sigma_1^1$ -formulae on finite structures. Annals of Pure and Applied Logic, 24:1–48, 1983.
- [3] Eric Allender, Harry Buhrman, Michal Kouckỳ, Dieter van Melkebeek, and Detlef Ronneburger. Power from random strings. *SIAM Journal on Computing*, 35(6):1467–1493, 2006.
- [4] Eric Allender and Bireswar Das. Zero knowledge and circuit minimization. *Information and Compution*, 256:2–8, 2017.
- [5] Eric Allender, Joshua A Grochow, Dieter van Melkebeek, Cristopher Moore, and Andrew Morgan. Minimum circuit size, graph isomorphism, and related problems. SIAM Journal on Computing, 47(4):1339–1372, 2018.
- [6] Eric Allender, Lisa Hellerstein, Paul McCabe, Toniann Pitassi, and Michael Saks. Minimizing disjunctive normal form formulas and AC<sup>0</sup> circuits given a truth table. SIAM Journal on Computing, 38(1):63–84, 2008.
- [7] Eric Allender and Shuichi Hirahara. New insights on the (non)-hardness of circuit minimization and related problems. *ACM Transactions on Computation Theory*, 11(4):27:1–27:27, 2019.
- [8] Eric Allender, Dhiraj Holden, and Valentine Kabanets. The minimum oracle circuit size problem. *computational complexity*, 26(2):469–496, Jun 2017.
- [9] Eric Allender, Rahul Ilango, and Neekon Vafa. The non-hardness of approximating circuit size. In *Proc. 14th International Computer Science Symposium in Russia (CSR)*, volume 11532 of *Lecture Notes in Computer Science*, pages 13–24. Springer, 2019.
- [10] Eric Allender, Michal Kouckỳ, Detlef Ronneburger, and Sambuddha Roy. The pervasive reach of resource-bounded Kolmogorov complexity in computational complexity theory. *J. Comput. Syst. Sci.*, 77(1):14–40, 2011.

- [11] Eric Allender, Michael C Loui, and Kenneth W Regan. Reducibility and completeness. In *Algorithms and theory of computation handbook*, pages 23–23. Chapman & Hall/CRC, 2010.
- [12] Sanjeev Arora. AC<sup>0</sup>-reductions cannot prove the PCP theorem. Unpublished Manuscript., 1995.
- [13] Merrick Furst, James B. Saxe, and Michael Sipser. Parity, circuits, and the polynomial-time hierarchy. *Mathematical Systems Theory*, 17(1):13–27, 1984.
- [14] Alexander Golovnev, Rahul Ilango, Russell Impagliazzo, Valentine Kabanets, Antonina Kolokolova, and Avishay Tal. AC<sup>0</sup>[p] lower bounds against MCSP via the coin problem. In *Proc. 46th International Colloquium on Automata, Languages, and Programming, (ICALP)*, volume 132 of *LIPIcs*, pages 66:1–66:15, 2019.
- [15] Johan Håstad. Computational Limitations for Small Depth Circuits. MIT Press, Cambridge, MA, 1987.
- [16] Johan Håstad. Some optimal inapproximability results. J. ACM, 48(4):798-859, 2001.
- [17] Pooya Hatami, Raghav Kulkarni, and Denis Pankratov. Variations on the sensitivity conjecture. *Theory of Computing, Graduate Surveys*, 4:1–27, 2011.
- [18] Shuichi Hirahara. Non-black-box worst-case to average-case reductions within NP. In *Proc. 59th IEEE Symposium on Foundations of Computer Science (FOCS)*, pages 247–258, 2018.
- [19] Shuichi Hirahara and Rahul Santhanam. On the average-case complexity of MCSP and its variants. In *Proc. 32nd Computational Complexity Conference (CCC)*, volume 79 of *LIPIcs*, pages 7:1–7:20. Schloss Dagstuhl-Leibniz-Zentrum fuer Informatik, 2017.
- [20] Shuichi Hirahara and Osamu Watanabe. Limits of minimum circuit size problem as oracle. In *Proc. 31st Computational Complexity Conference (CCC)*, volume 50 of *LIPIcs*, pages 18:1–18:20. Schloss Dagstuhl–Leibniz-Zentrum fuer Informatik, 2016.
- [21] John Hitchcock and Aduri Pavan. On the NP-completeness of the minimum circuit size problem. In *Proc. 35th IARCS Annual Conference*

- on Foundation of Software Technology and Theoretical Computer Science (FSTTCS), volume 45 of LIPIcs, pages 236–245. Schloss Dagstuhl-Leibniz-Zentrum fuer Informatik, 2015.
- [22] Rahul Ilango. Approaching MCSP from above and below: Hardness for a conditional variant and AC<sup>0</sup>[p]. In *Proc. 11th Innovations in Theoretical Computer Science Conference, (ITCS)*, volume 151 of *LIPIcs*, pages 34:1–34:26. Schloss Dagstuhl Leibniz-Zentrum für Informatik, 2020.
- [23] Russell Impagliazzo, Valentine Kabanets, and Ilya Volkovich. The power of natural properties as oracles. In *Proc. 33rd Computational Complexity Conference (CCC)*, volume 102 of *LIPIcs*, pages 7:1–7:20. Schloss Dagstuhl-Leibniz-Zentrum fuer Informatik, 2018.
- [24] Valentine Kabanets and Jin-Yi Cai. Circuit minimization problem. In *Proc. 32nd ACM Symposium on Theory of Computing (STOC)*, pages 73–79, New York, NY, USA, 2000.
- [25] Dylan M. McKay, Cody D. Murray, and R. Ryan Williams. Weak lower bounds on resource-bounded compression imply strong separations of complexity classes. In *Proc. 51st ACM Symposium on Theory of Computing (STOC)*, pages 1215–1225. ACM, 2019.
- [26] Cody D. Murray and R. Ryan Williams. On the (non) NP-hardness of computing circuit complexity. *Theory of Computing*, 13(1):1–22, 2017.
- [27] Igor Carboni Oliveira, Ján Pich, and Rahul Santhanam. Hardness magnification near state-of-the-art lower bounds. In *Proc. 34th Computational Complexity Conference (CCC)*, volume 137 of *LIPIcs*, pages 27:1–27:29. Schloss Dagstuhl–Leibniz-Zentrum fuer Informatik, 2019.
- [28] Igor Carboni Oliveira and Rahul Santhanam. Conspiracies between learning algorithms, circuit lower bounds and pseudorandomness. In *Proc. 32nd Computational Complexity Conference (CCC)*, volume 79 of *LIPIcs*, pages 18:1–18:49. Schloss Dagstuhl–Leibniz-Zentrum fuer Informatik, 2017.
- [29] Igor Carboni Oliveira and Rahul Santhanam. Hardness magnification for natural problems. In *Proc. 59th IEEE Symposium on Foundations of Computer Science (FOCS)*, pages 65–76, 2018.
- [30] Alexander A. Razborov and Steven Rudich. Natural proofs. *J. Comput. Syst. Sci.*, 55(1):24–35, 1997.

- [31] Michael Rudow. Discrete logarithm and minimum circuit size. *Information Processing Letters*, 128:1–4, 2017.
- [32] Boris Trakhtenbrot. A survey of Russian approaches to perebor (brute-force searches) algorithms. *IEEE Ann. Hist. Comput.*, 6(4):384–400, October 1984.
- [33] Heribert Vollmer. Introduction to circuit complexity: a uniform approach. Springer Science & Business Media, 2013.